

In the Claims

1. (original) An input circuit for an RF power amplifier comprising:  
an input network having a transformer with a primary side and a secondary side, wherein an RF  
input signal is coupled to the primary side;  
a limiting amplifier having an input coupled to the secondary side of the transformer and an  
output for providing an input to the RF power amplifier; and  
a DC feedback loop coupled to the limiting amplifier.

-Claim 2 (canceled).

- PN 3. (currently amended) The input circuit of ~~claim 2~~<sup>21</sup> claim 2, further comprising an  
amplifying stage coupled between the input of the limiting amplifier and the transformer.

4. (original) The input circuit of claim 1, further comprising an amplifying stage  
coupled between the input of the limiting amplifier and the transformer.

5. (original) The input circuit of claim 4, wherein the amplifying stage is comprised of  
a common-source amplifier.

6. (original) The input circuit of claim 4, wherein the amplifying stage is comprised of  
a common-gate amplifier having one or more switching devices.

7. (original) The input circuit of claim 6, wherein a bias is provided to a gate terminal  
of each of the one or more switching devices.

8. (original) The input circuit of claim 7, wherein the bias is a DC bias.